PATENT

IN THE UNITED STATES PATENT OFFICE

Applicant: Stephen M. Trimberger

Assignee: Xilinx, Inc.

Title: "Quintuple Modular Redundancy for High reliability

Circuits Implemented in Programmable Logic

Devices"

Serial No.: Not Yet Known File Date: 02-26-04

Examiner: Not Yet Known Art Unit: Not Yet Known

Divisional of

Serial No.: 10/245,741 File Date: 09-16-02

Docket No.: X-1102-1P-1D US

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INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicant brings to the attention of the Examiner the ten (10) references listed in the attached Substitute for Form PTO-1449 (Information Disclosure Statement by Applicant).

All of these references were cited in prior related U.S. patent application Serial Number 10/245,741 filed September 16, 2002 to which this application claims priority. Copies of these references have not been supplied herein since they were previously submitted in the parent case.

Citation of the above documents shall not be construed as an admission that the documents are necessarily prior art with respect to the instant invention. Citation of the above documents shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above documents shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

Respectfully Submitted,

Lois D. Cartier Agent for Applicant Reg. No. 40,941

LDC:pbs

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Substitute for form 1449A/PTO		Complete if Known			
Substitute	101 101111 1449/01	-10		Application / Conf. No.	Unknown /
INFO	RMATION	DI	SCLOSURE	Filing Date	February 26, 2004
STATEMENT BY APPLICANT (use as many sheets as necessary)			APPLICANT	First Named Inventor	Stephen M. Trimberger
			AII LIOAIII	Art Unit	Unknown
			necessary)	Examiner Name	Unknown
Sheet	1	of	2	Attorney Docket Number	X-1102-1P-1D US

	6 ''	Document Number	U.S. PATENT Publication Date	Name of Patentee or Applicant of Cited Document	Pages Columns Lines Where	
Examiner Initials *	Cite No.¹	Number - Kind Code (if known)	MM-DD-YYYY		Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	
		US- Re 34,363	08-31-93	Freeman		
		US- 4,695,740	09-22-87	Carter		
		US- 4,713,557	12-15-87	Carter		
		US- 4,835,418	05-30-87	Hsieh		
		US- 5,031,180	07-09-91	McIver et al.		
		US- 5,517,135	05-14-96	Young		
		US- 5,925,920	07-20-99	MacArthur et al.		
		US- 6,104,211	08-15-00	Alfke		
		US- 6,526,559 B2	02-25-03	Schiefele et al.		
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	FOREIGN PATENT DOCUMENTS							
Examiner	Cite	Foreign Patent Document	Publication Date	Name of Patentee or	Pages, Columns, Lines,	T 6		
Initials * No.1		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)	MM-DD-YYYY	Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear			
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¹ Applicant's unique citation designation number (optional). ² See Kinds of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁵ Applicant is to place a check mark here if English language Translation is attached.

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PTO/SB/08A (10-01)
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Application / Conf. No. Substitute for form 1449A/PTO INFORMATION DISCLOSURE **Filing Date** February 26, 2004 STATEMENT BY APPLICANT **First Named Inventor** Stephen M. Trimberger Art Unit Unknown (use as many sheets as necessary) **Examiner Name** Unknown

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of

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OTHER NON PATENT LITERATURE DOCUMENTS				
Examiner Initials *	Cite No 1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²	
		Seong Woo Kwak and Byung Kook Kim, "Task-Scheduling Strategies for Reliable TMR Controllers Using Task Grouping and Assignment"; IEEE Transactions on Reliability, Vol. 49, No. 4; December 2000; pp. 355-362.		
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